

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	110	esd and (third with path) and voltage and (input pad pin) and float\$3 and (clamp \$3 shunt \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/03/25 13:21
L2	324	esd and (third with path) and voltage and (input pad pin) and (power supply float \$3) and (clamp\$3 shunt\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/03/25 13:24
L3	28	esd and (third with path) and voltage and (input pad pin) and ((power supply) with float \$3) and (clamp\$3 shunt\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/03/25 13:24

L4	34	esd and (third with path) and voltage and (input pad pin) and (power supply line) with (float floating floats) and (clamp\$3 shunt\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/03/25 13:25
L5	194	esd and (second with diode) and voltage and (input pad pin) and (power supply line) with (float floating floats) and (clamp\$3 shunt\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/03/25 13:28
L6	194	esd and (second with diode) and voltage and (input pad pin) and ((power supply line) with (float floating floats)) and (clamp \$3 shunt \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/03/25 13:29

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	4	(esd and (second with diode) and voltage and (input pad pin) and ((power supply line) with (float floating floats)) and (clamp\$3 shunt\$3)). clm.	US-PGPUB; USPAT; UPAD	OR	ON	2011/03/25 13:33
L8	3622	361/56.ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2011/03/25 13:38

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